

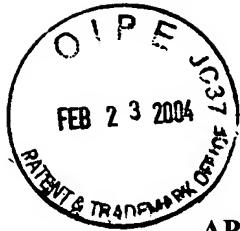


**APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 10/698,633, filed 10/31/03; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:						
Efficient ESD Protection with Application for Low Capacitance I/O Pads						
POWER OF ATTORNEY: I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER 23494 TO PROSECUTE THIS APPLICATION AND TRANACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH						
SEND CORRESPONDENCE TO:		DIRECT TELEPHONE CALLS TO:				
Gary C. Honeycutt Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		Gary C. Honeycutt (214) 939-8651				
NAME OF INVENTOR:	(1)	NAME OF INVENTOR:	(2)	NAME OF INVENTOR:	(3)	
Craig T. Salling		Charvaka Duvvury		N/A		
RESIDENCE & POST OFFICE ADDRESS:	RESIDENCE & POST OFFICE ADDRESS:		RESIDENCE & POST OFFICE ADDRESS:			
1404 Chatsworth Lane ^{910 Provence Ct.} Plano, Texas 75075 ⁹⁵⁰¹⁴		6821 Oak Park Lane Plano, Texas 75023				
COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:		COUNTRY OF CITIZENSHIP:			
United States		United States				
SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:		SIGNATURE OF INVENTOR:			
DATE:	DATE:		DATE:			
x DEC-03-2003						



APPLICATION FOR UNITED STATES PATENT
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TITLE OF INVENTION:

Efficient ESD Protection with Application for Low Capacitance I/O Pads

POWER OF ATTORNEY: I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER **23494** TO PROSECUTE THIS APPLICATION AND TRANACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH

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NAME OF INVENTOR:	(1)	NAME OF INVENTOR:	(2)
Craig T. Salling		Charvaka Duvvury	
NAME OF INVENTOR:	(3)	NAME OF INVENTOR:	(3)
N/A		N/A	
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1404 Chatsworth Lane Plano, Texas 75075		6821 Oak Park Lane Plano, Texas 75023	
COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:		COUNTRY OF CITIZENSHIP:
United States		United States	
SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:		SIGNATURE OF INVENTOR:
x	x <i>Charvaka Duvvury</i>		
DATE:	DATE:		DATE:
x	x November 19, 2003		